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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10070536	FILING DATE 07/02/2002	CLASS 257	SUBCLASS :	GAU 28 4	EXAMINER <i>Loke</i>
**APPLICANTS: Sakamoto Kazuhisa;					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A 371 OF PCT/JP00/06035 09/06/2000 <i>Loke</i>					
** FOREIGN APPLICATIONS VERIFIED: JAPAN 11/255881 09/09/1999 <i>Loke</i>					
PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>			
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials <i>Loke</i>				ATTORNEY DOCKET NO ROH-051	
TITLE : Semiconductor device					

U.S. DEPT. OF COMM. PAT & TM-PTO 436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED.		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
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